


Multi-Objective Optimization in Power Electronics

Presentation

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Multi-Objective Optimization in Power Electronics

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Outline

- ▶ Global Megatrends
- ▶ Resulting Requirements for Power Electronics
- ▶ *Multi-Objective Optimization Approach*
- ▶ Optimization Application Example
- ▶ Summary

Acknowledgement

D. Bortis
R. Bosshard
R. Burkart
F. Krismer

Global Megatrends



*Climate Change
Digitalization
Sustainable Mobility
Urbanization
Etc.*

Global Megatrends

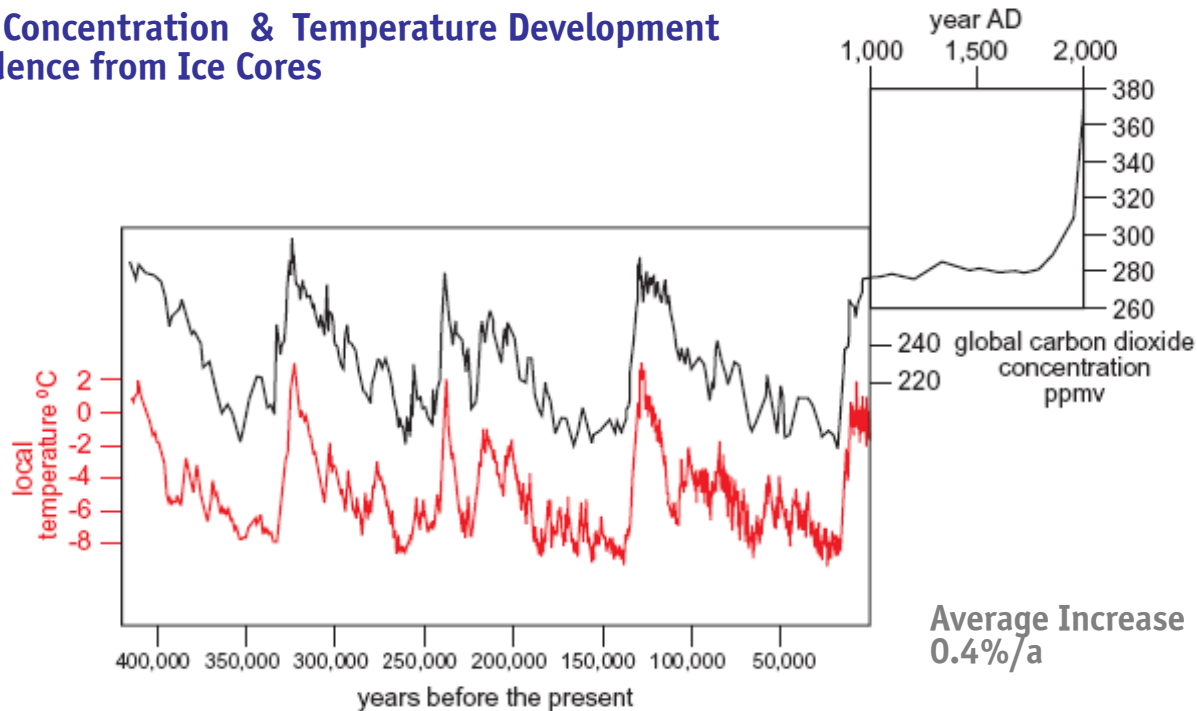


Climate Change →

Digitalization
Sustainable Mobility
Urbanization
Etc.

► Climate Change

- CO₂ Concentration & Temperature Development
- Evidence from Ice Cores



- Reduce CO₂ Emissions *Intensity* (CO₂/GDP) to Stabilize Atmospheric CO₂ Concentration
- 1/3 in 2050 → less than 1/10 in 2100 (AIST, Japan @ IEA Workshop 2007)

► Climate Change

- CO₂ Concentration & Temperature Development
- Evidence from Ice Cores



Source: H. Nilsson
Chairman IEA DSM Program
FourFact AB

- Reduce CO₂ Emissions *Intensity* (CO₂/GDP) to Stabilize Atmospheric CO₂ Concentration
- 1/3 in 2050 → less than 1/10 in 2100 (AIST, Japan @ IEA Workshop 2007)

→ Utilize Renewable Energy (1)

■ Enabled by Power Electronics

- Higher Reliability (!)
- Lower Costs

Source: M. Prahm / Flickr

Medium-Voltage Power
Collection and Connection
to On-Shore Grid

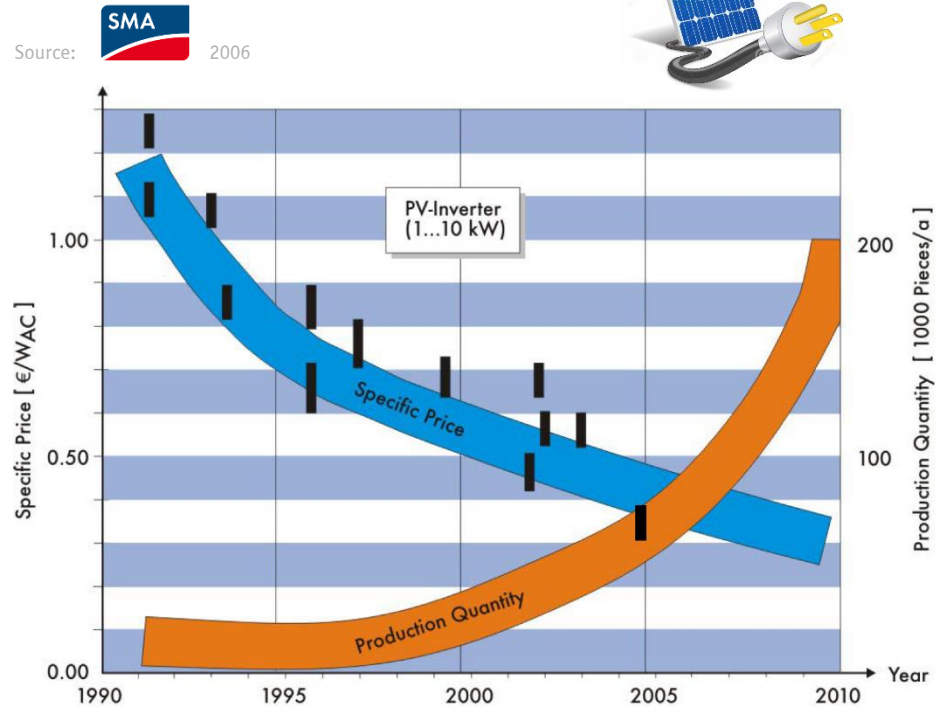


- ▶ Off-Shore Wind Farms
- ▶ Medium Voltage Systems

→ Utilize Renewable Energy (2)

■ Enabled by Power Electronics

- Extreme Cost Pressure (!)
- Higher Efficiency
- Higher Power Density



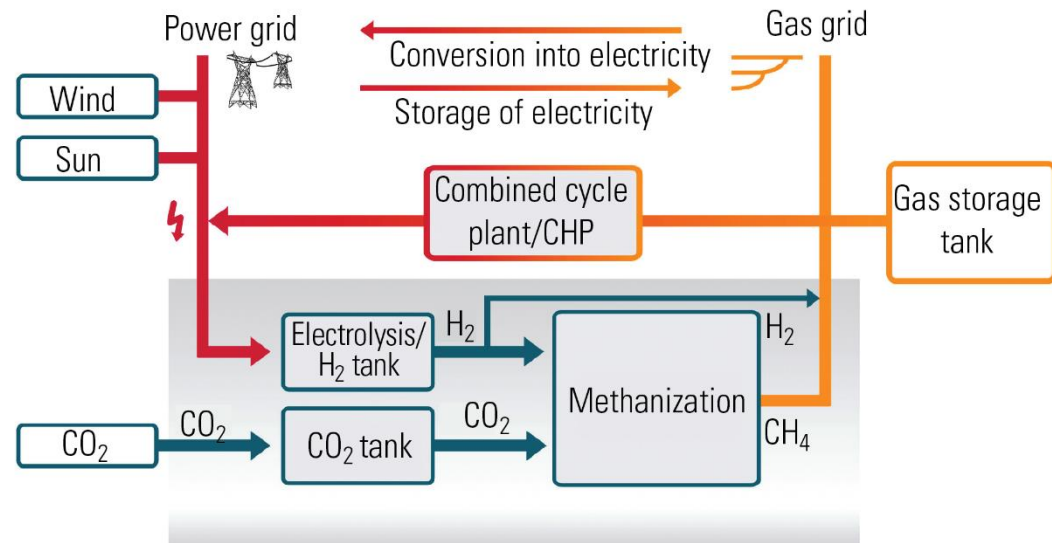
- ▶ Photovoltaics Power Plants
- ▶ Up to Several MW Power Level
- ▶ Future Hybrid PV/Therm. Collectors

→ Utilize Renewable Energy (3)

■ Enabled by Power Electronics

- Electrolysis for Conversion of Excess Wind/Solar Electric Energy into Hydrogen
 - Fuel-Cell Powered Cars
 - Heating

Hydrogenics 100 kW
H₂-Generator ($\eta=57\%$),
High Power @ Low
Voltage



Source: www.r-e-a.net

Global Megatrends



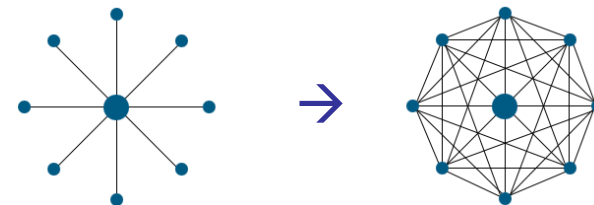
Climate Change
Digitalization →
Sustainable Mobility
Urbanization
Etc.

► Digitalization

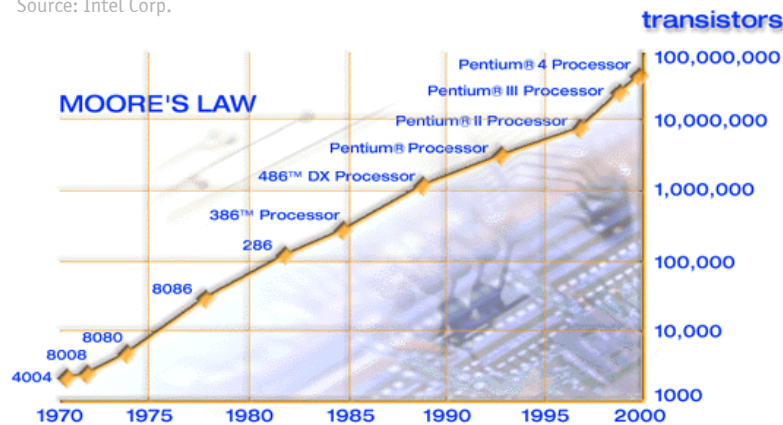
■ Internet of Things (IoT) / Cognitive Computing

- Ubiquitous Computing / BIG DATA
- Fully Automated Manufacturing / Industry 4.0
- Autonomous Cars
- Etc.

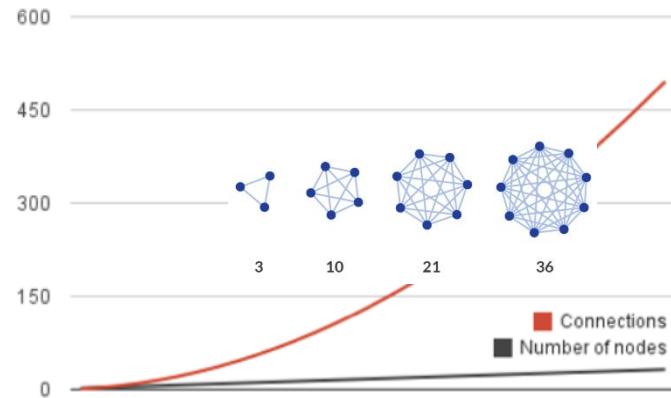
– Moving from Hub-Based to Community Concept Increases Potential Network Value Exponentially ($\sim n(n-1)$ or $\sim n \log(n)$)



Source: Intel Corp.



► Moore's Law



► Metcalfe's Law

→ Green / Zero Datacenters (1)

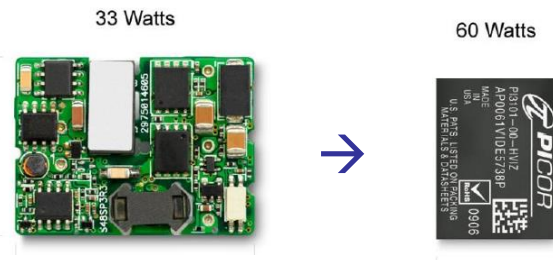
■ Enabled by Power Electronics

- Ranging from Medium Voltage to Power-Supplies-on-Chip
- Short Power Supply Innovation Cycles
- Modularity / Scalability
- Higher Power Density (!)
- Higher Efficiency (!)
- Lower Costs

Server-Farms
up to 450 MW
99.9999% / <30s/a
\$1.0 Mio./Shutdown

Since 2006
Running Costs >
Initial Costs

Source: REUTERS/Sigtryggur Ari

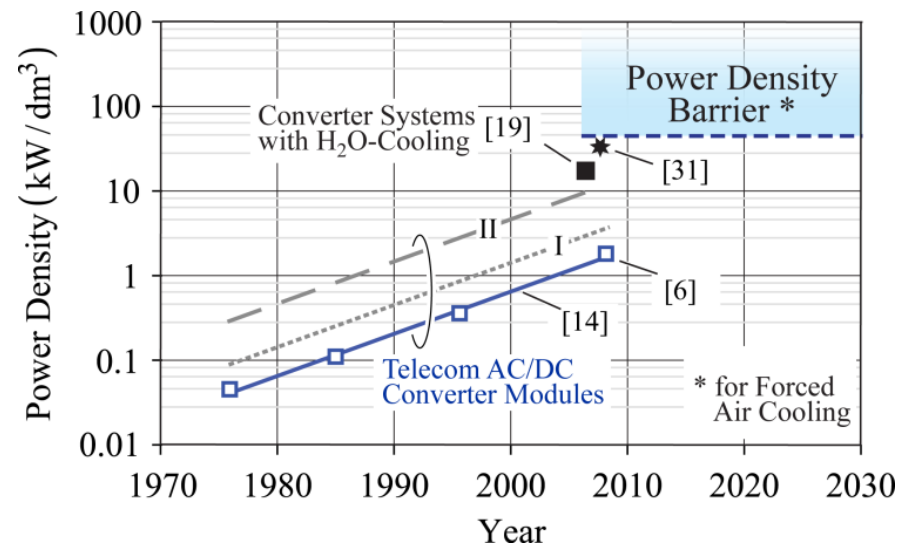


→ Green / Zero Datacenters (2)

■ Enabled by Power Electronics

- Ranging from Medium Voltage to Power-Supplies-on-Chip
- Short Power Supply Innovation Cycles
- Modularity / Scalability
- Higher Power Density (!)
- Higher Efficiency (!)
- Lower Costs

► Power Density Increased by Factor 2 over 10 Years

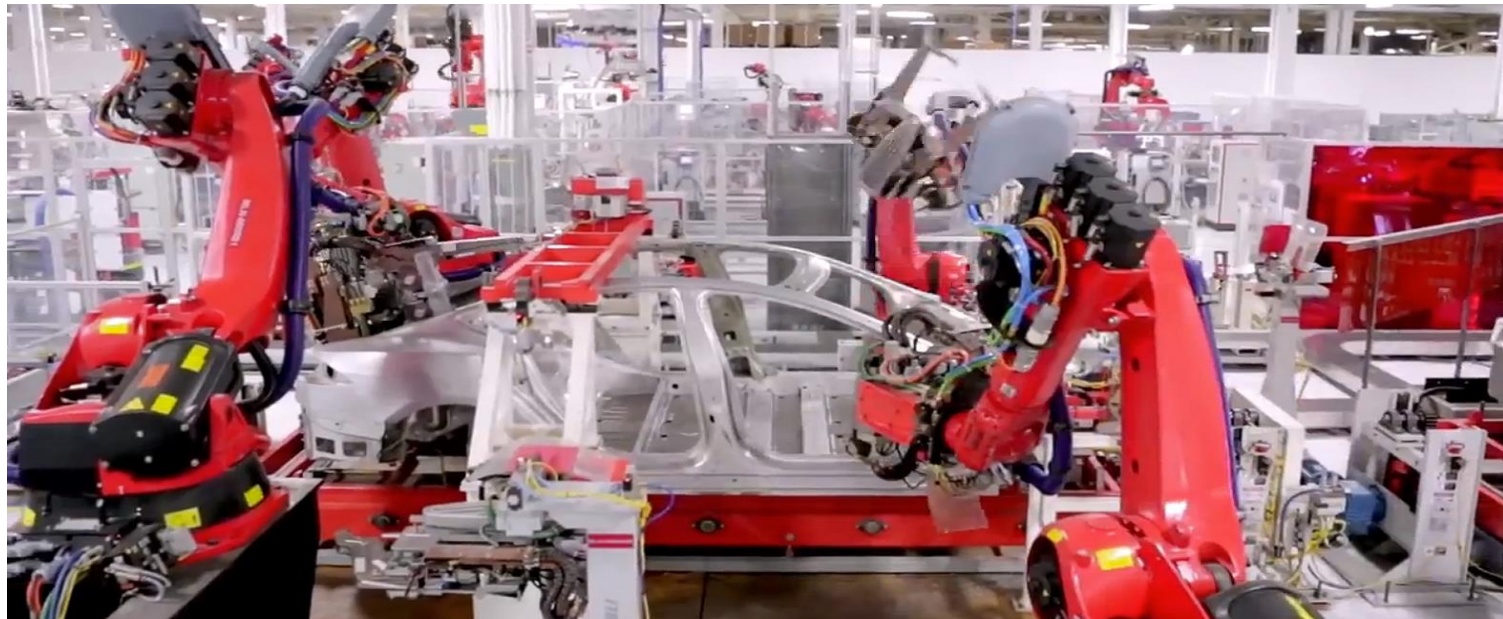


→ Fully Automated Manufacturing – Industry 4.0

■ Enabled by Power Electronics

- Lower Costs (!)
- Higher Power Density
- Self-Sensing etc.

Source:  TESLA MOTORS

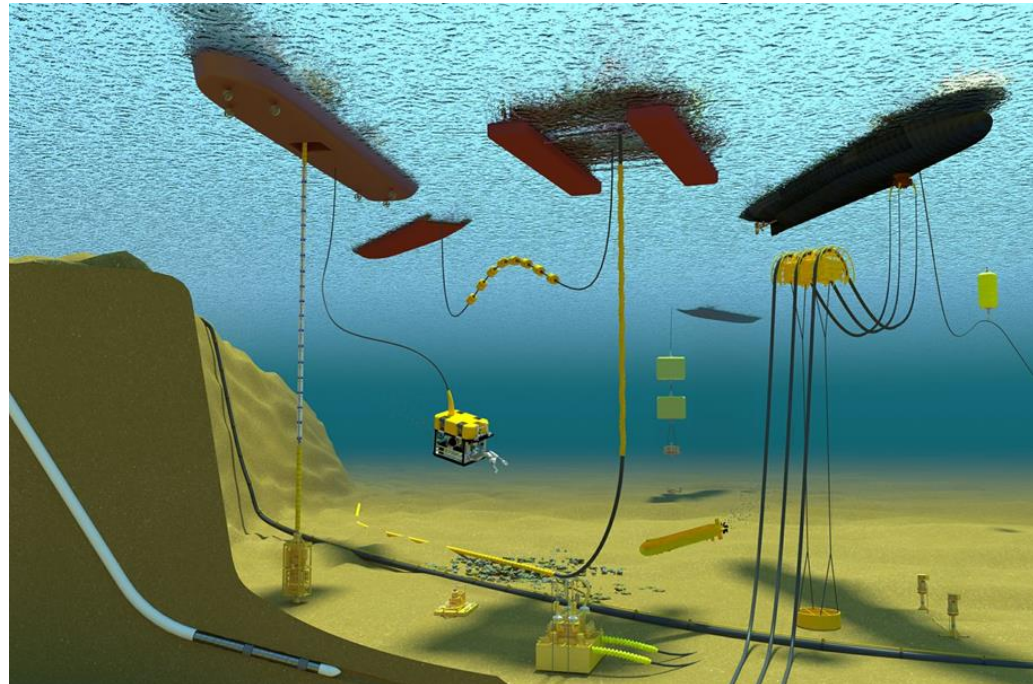


→ Fully Automated Raw Material Extraction

■ Enabled by Power Electronics

- High Reliability (!)
- High Power Density (!)

Source: matrixengineered.com



- ▶ ABB's Future Subsea Power Grid → "Develop All Elements for a Subsea Factory"

Global Megatrends



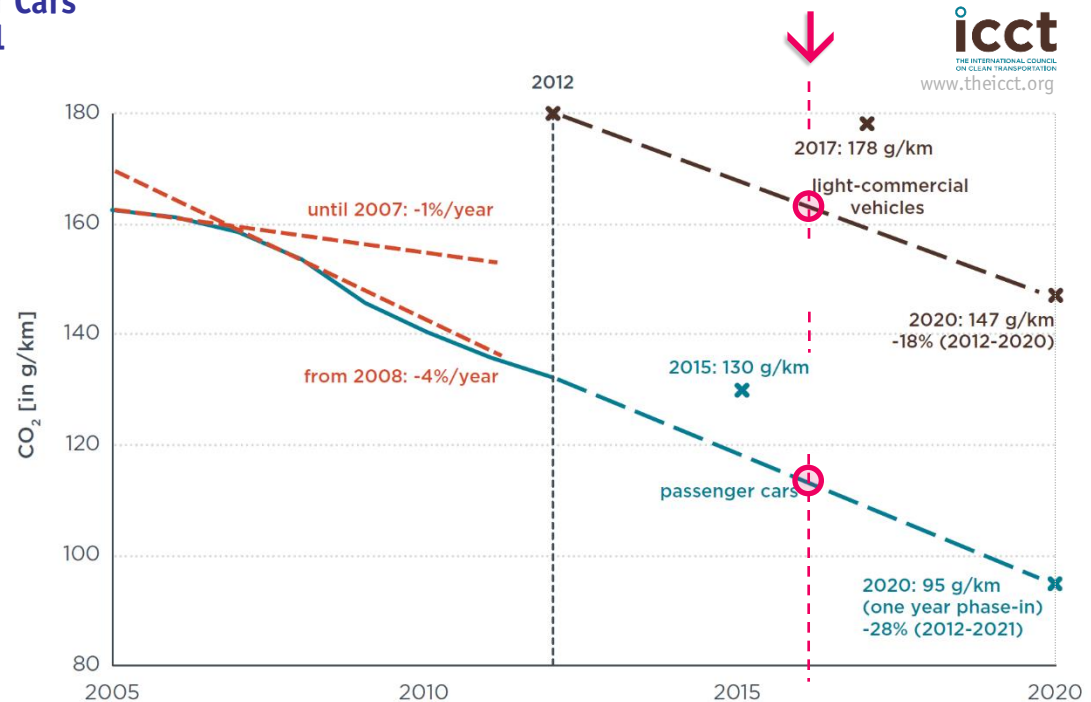
Climate Change
Digitalization
Sustainable Mobility
Urbanization
Etc.



► Sustainable Mobility

■ EU Mandatory 2020 CO₂ Emission Targets for New Cars

- 147g CO₂/km for Light-Commercial Vehicles
- 95g CO₂/km for Passenger Cars
- 100% Compliance in 2021



- Hybrid Vehicles
- Electric Vehicles

→ Electric Vehicles (1)

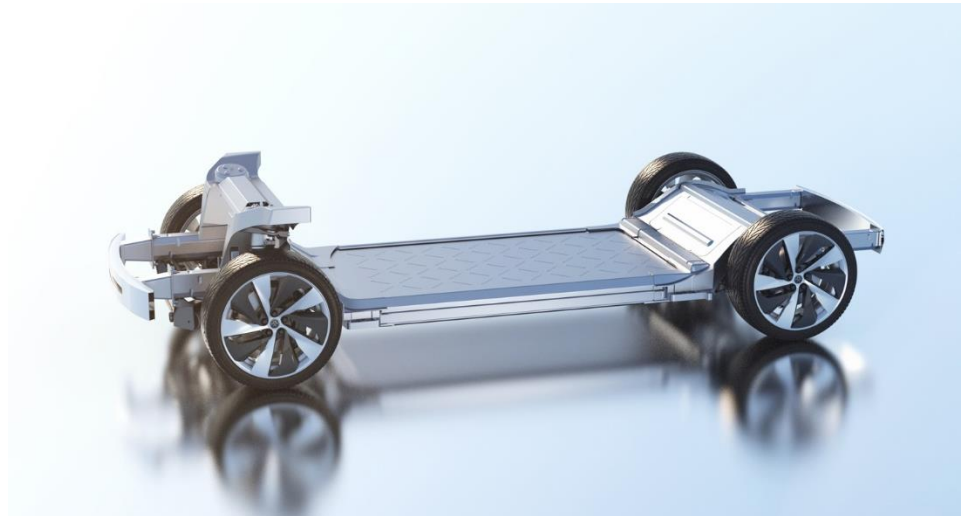
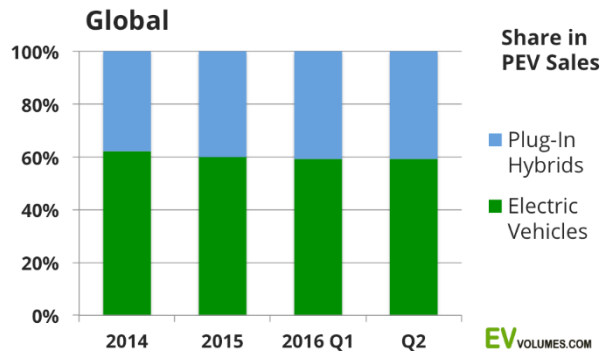
■ Enabled by Power Electronics - Drivetrain / Aux. / Charger

- Higher Power Density
- Extreme Cost Pressure (!)



Faraday Future

FF-ZERO1
750kW / 322km/h
1 Motor per Wheel
Lithium-Ion Batteries along the Floor

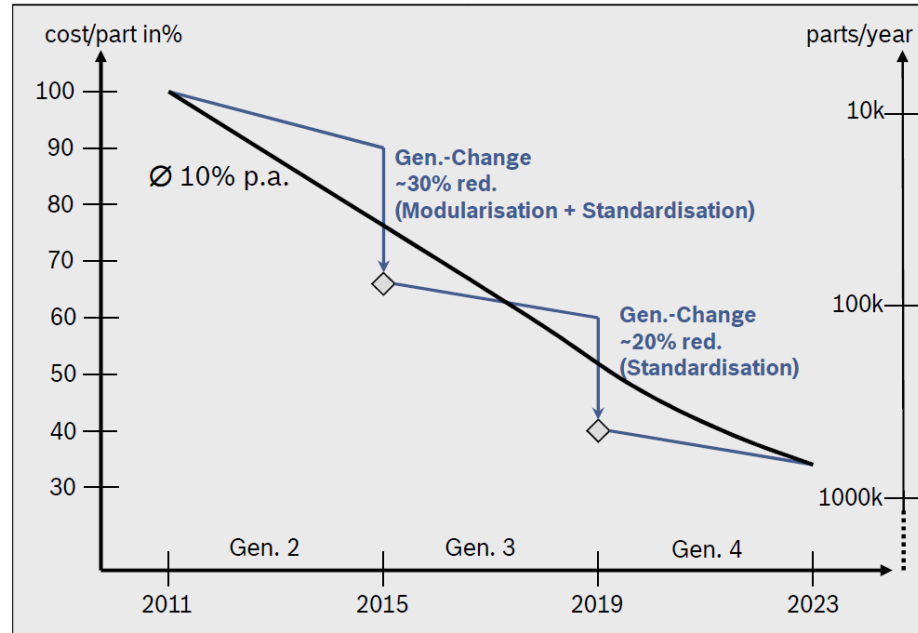


→ Electric Vehicles (2)

■ Enabled by Power Electronics - Drivetrain / Aux. / Charger

- Higher Power Density
- Extreme Cost Pressure (!)

Source: PCIM 2013



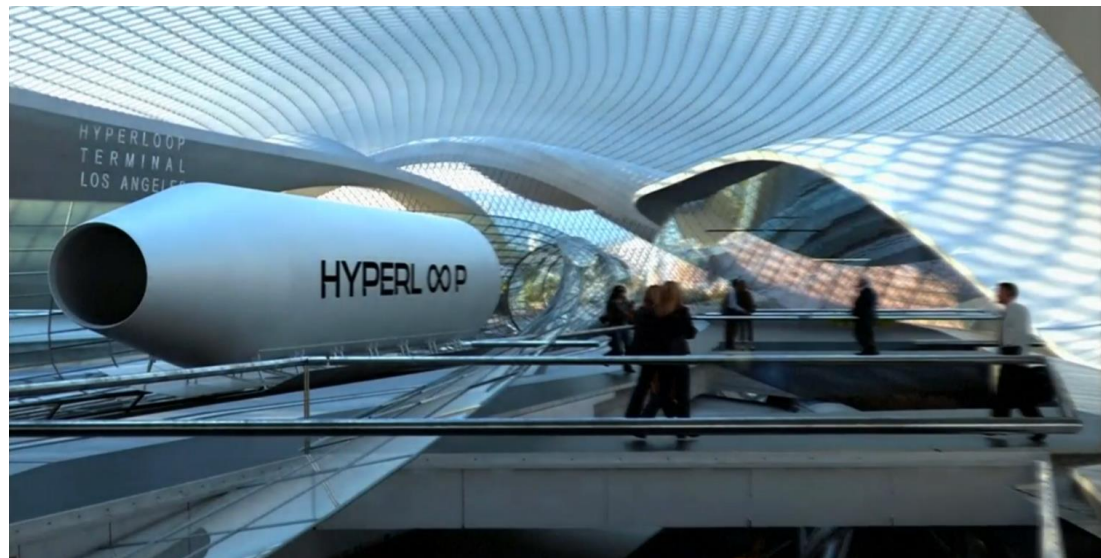
- ▶ Typ. 10% / a Cost Reduction
- ▶ Economy of Scale !

→ Futuristic Mobility Concepts (1)

■ Enabled by Power Electronics

- Hyperloop
- San Francisco → Los Angeles in 35min

 HYPERLOOP
POD COMPETITION
www.spacex.com/hyperloop



- ▶ Low Pressure Tube
- ▶ Magnetic Levitation
- ▶ Linear Ind. Motor
- ▶ Air Compressor in Nose

→ *Futuristic Mobility Concepts (2)*

■ Enabled by Power Electronics

— Cut Emissions Until 2050

- * CO₂ by 75%,
- * NO_x by 90%,
- * Noise Level by 65%

Source:

EADS

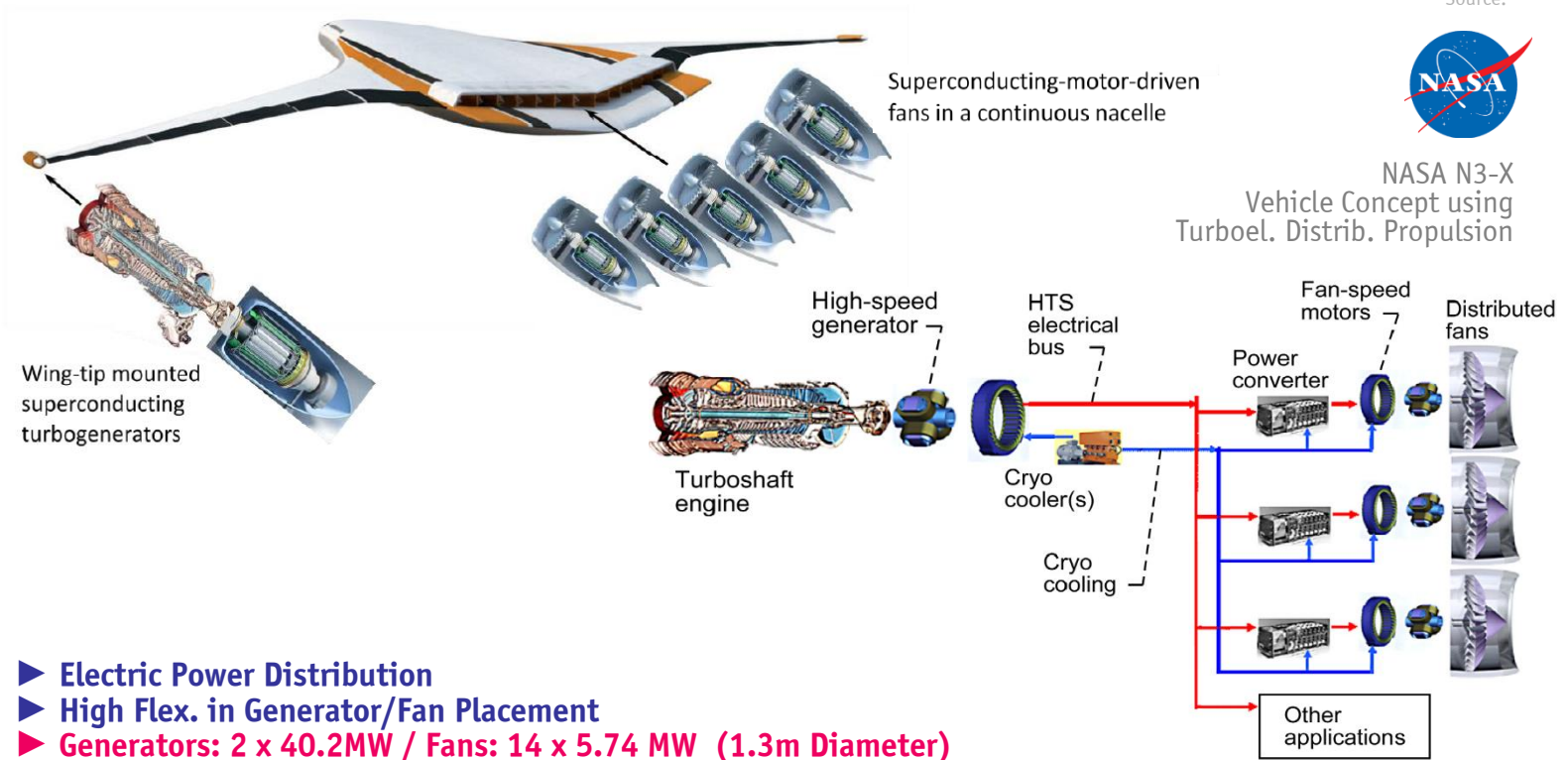
Future Hybrid
Distributed Propulsion Aircraft



- ▶ Eff. Optim. Gas Turbine
- ▶ 1000Wh/kg Batteries
- ▶ Distrib. Fans (E-Thrust)
- ▶ Supercond. Motors
- ▶ Med. Volt. Power Distrib.

→ Futuristic Mobility Concepts (3)

■ Enabled by Power Electronics



Global Megatrends



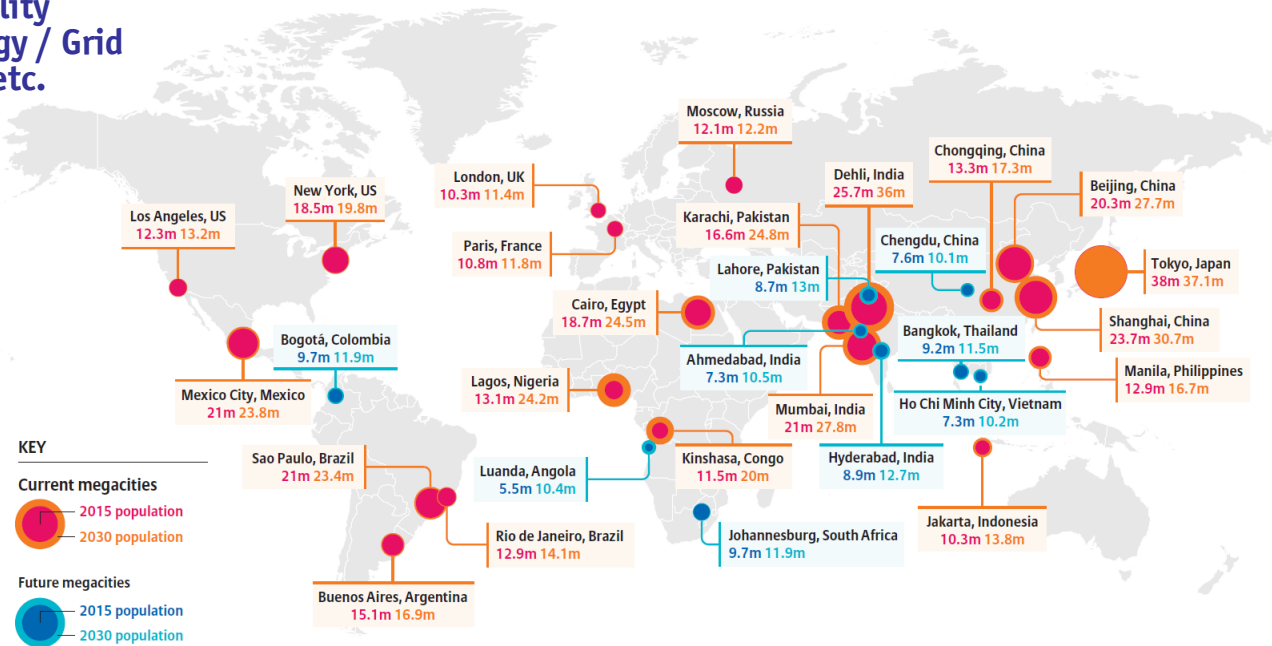
Climate Change
Digitalization
Sustainable Mobility
Urbanization →
Etc.

► Urbanization

- 60% of World Population Exp. to Live in Urban Cities by 2025
- 30 MEGA Cities Globally by 2023

- Smart Buildings
- Smart Mobility
- Smart Energy / Grid
- Smart ICT, etc.

Source: World Urbanization Prospects: The 2014 Revision



► Selected Current & Future MEGA Cities 2015 → 2030

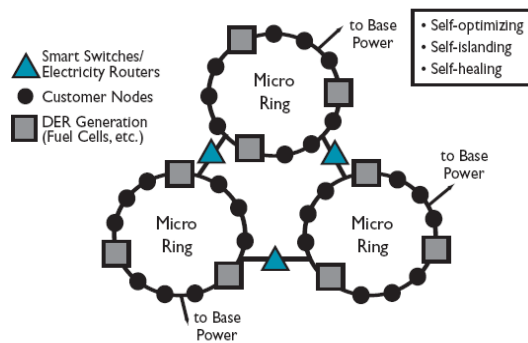
→ Smart Cities / Grid (1)

■ Enabled by Power Electronics

- Masdar = "Source"
- Fully Sustainable Energy Generation
 - * Zero CO₂
 - * Zero Waste
- EV Transport / IPT Charging
- to be finished 2025



Source: EPRI | ELECTRIC POWER RESEARCH INSTITUTE



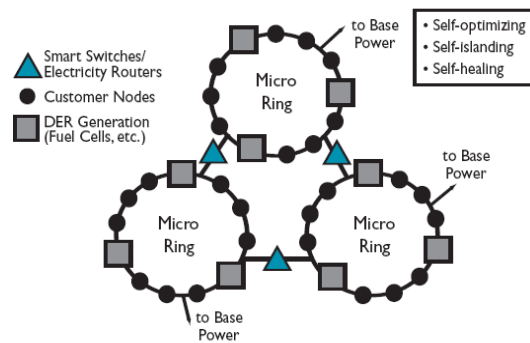
→ Smart Cities / Grid (2)

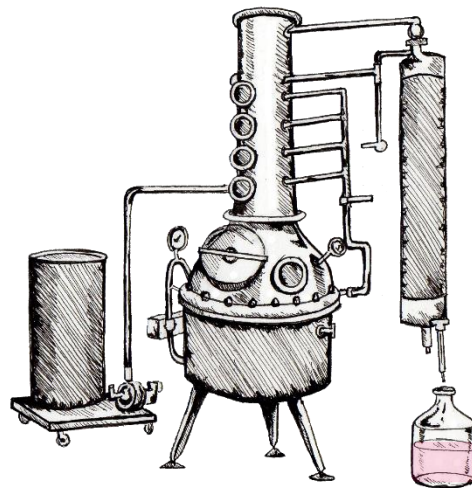
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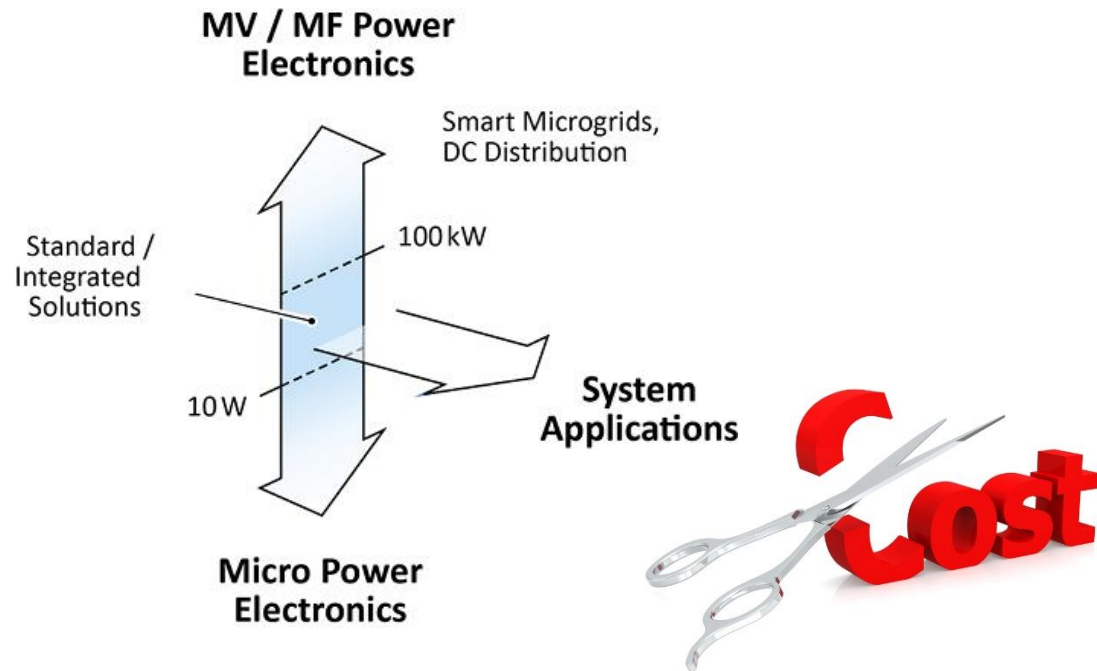


Source: whiskeybehavior.info

... in **Summary**

► Current / New Application Areas (2)

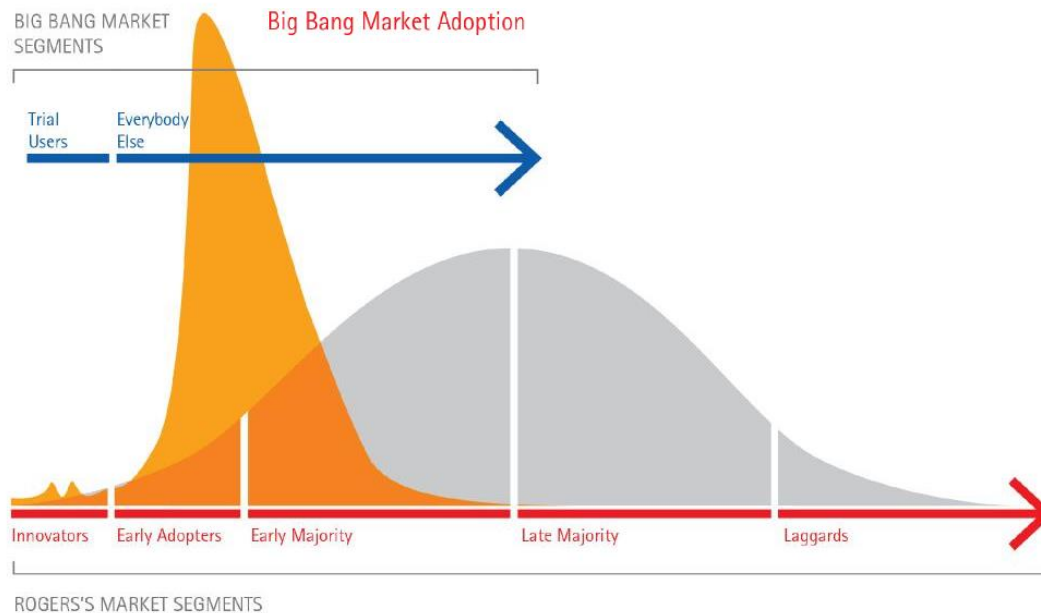
- Commodity / Standardization for High Volume Applications
- Extension to Microelectronics-Technology (Power Supply on Chip)
- Extensions to MV/MF



- Cost Pressure as Common Denominator of All Applications (!)
- Key Importance of Technology Partnerships of Academia & Industry

► Future “Big-Bang” Disruptions

- “Catastrophic” Success of Disruptive New (Digital) Technologies
- No Bell-Curve Technology Adoption / Technology S-Curve
- “Shark Fin“-Model



Source: www.verschuerent.wordpress.com
February 2015

See also:
Big Bang Disruption –
Strategy in the Age of
Devastating Innovation,
L. Downes and P. Nunes

► **Consequence: Market Immediately & Be Ready to Scale Up — and Exit — Swiftly (!)**

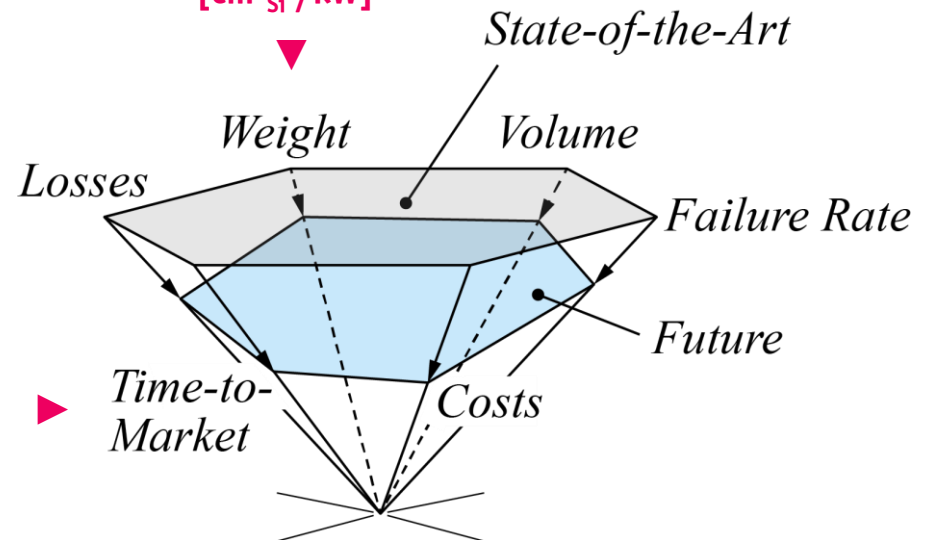
► Required Power Electronics Performance Improvements

Environmental Impact...

$[kg_{Fe} / kW]$
 $[kg_{Cu} / kW]$
 $[kg_{Al} / kW]$
 $[cm^2_{Si} / kW]$

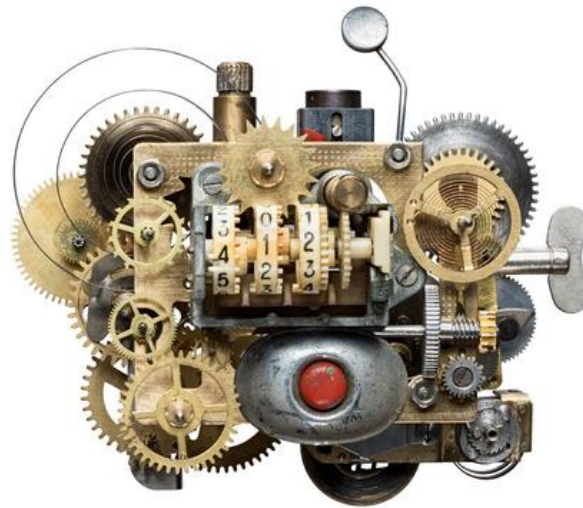
■ Performance Indices

- Power Density $[kW/dm^3]$
- Power per Unit Weight $[kW/kg]$
- Relative Costs $[kW/\$]$
- Relative Losses $[\%]$
- Failure Rate $[h^{-1}]$



► Multi-Objective Design Challenge

- Counteracting Effects of Key Design Parameters
- Mutual Coupling of Performance Indices → Trade-Offs



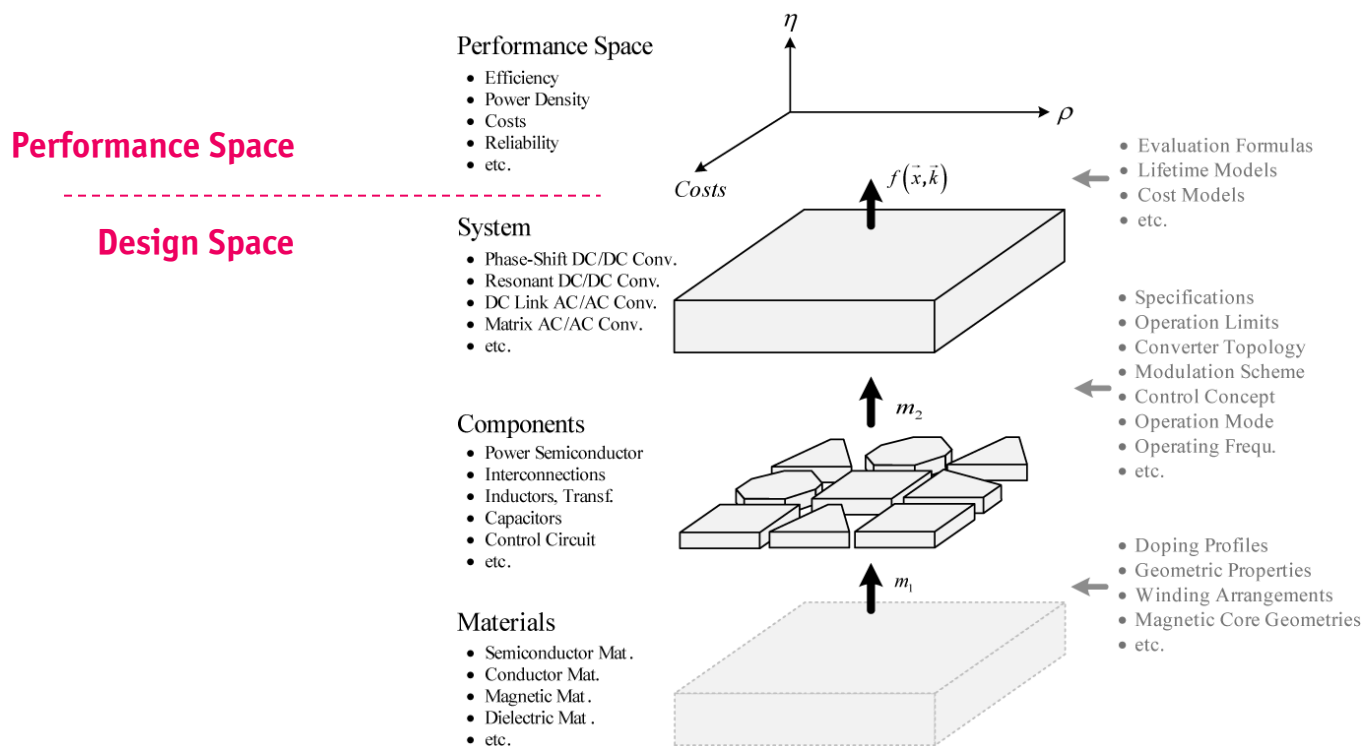
- Large Number of Degrees of Freedom / Multi-Dimensional Design Space
- Full Utilization of Design Space only Guaranteed by Multi-Objective Optimization



Multi-Objective Optimization

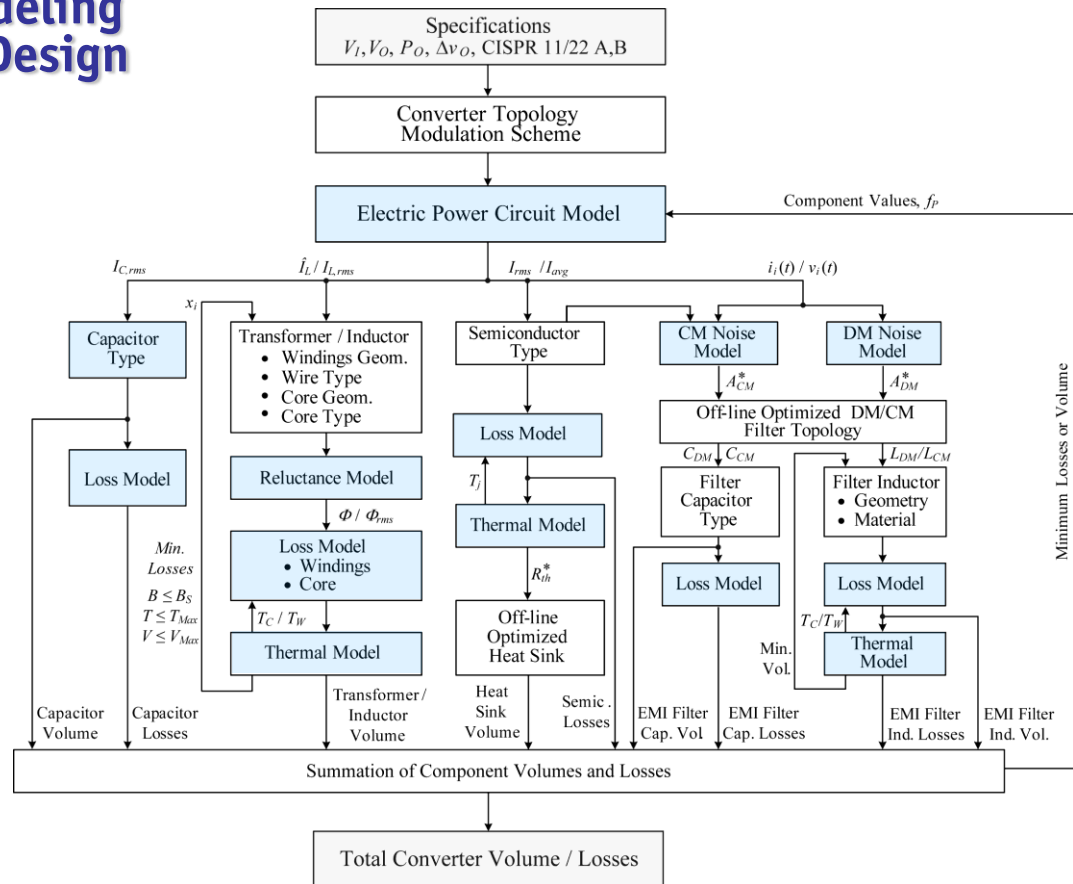
Abstraction of Converter Design
Design Space / Performance Space
Pareto Front
Sensitivities / Trade-Offs

► Abstraction of Power Converter Design



→ Mapping of "Design Space" into System "Performance Space"

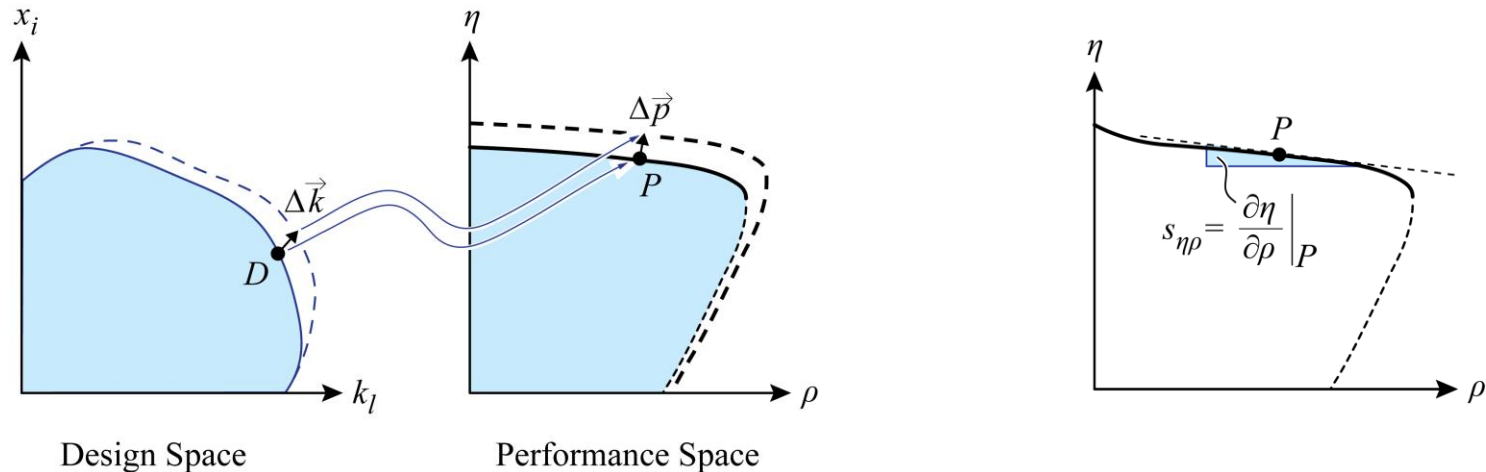
► Mathematical Modeling of the Converter Design



→ Multi-Objective Optimization – Guarantees Best Utilization of All Degrees of Freedom (!)

► Multi-Objective Optimization (1)

- Ensures **Optimal Mapping** of the “Design Space” into the “Performance Space”
- Identifies **Absolute Performance Limits** → **Pareto Front / Surface**

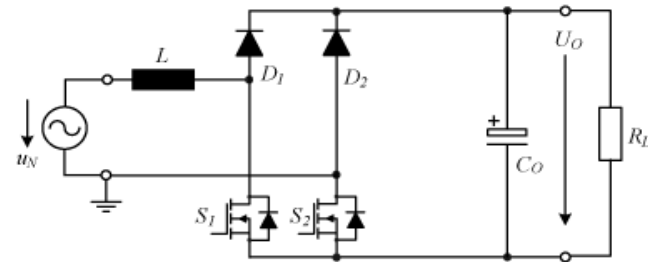


- Clarifies **Sensitivity** $\Delta \vec{p} / \Delta \vec{k}$ to Improvements of Technologies
- **Trade-off Analysis**

► Determination of the η - ρ -Pareto Front (a)

■ Comp.-Level Degrees of Freedom of the Design

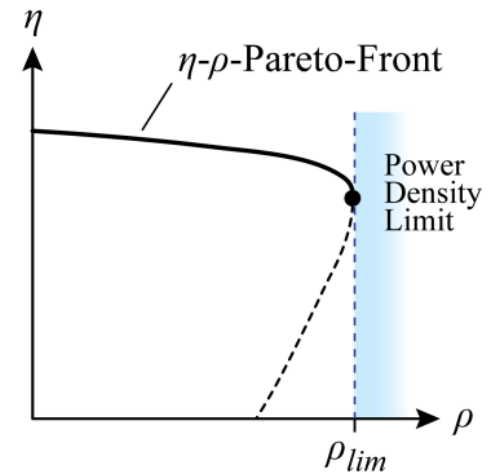
- Core Geometry / Material
- Single / Multiple Airgaps
- Solid / Litz Wire, Foils
- Winding Topology
- Natural / Forced Conv. Cooling
- Hard-/Soft-Switching
- Si / SiC
- etc.
- etc.
- etc.



■ System-Level Degrees of Freedom

- Circuit Topology
- Modulation Scheme
- Switching Frequ.
- etc.
- etc.

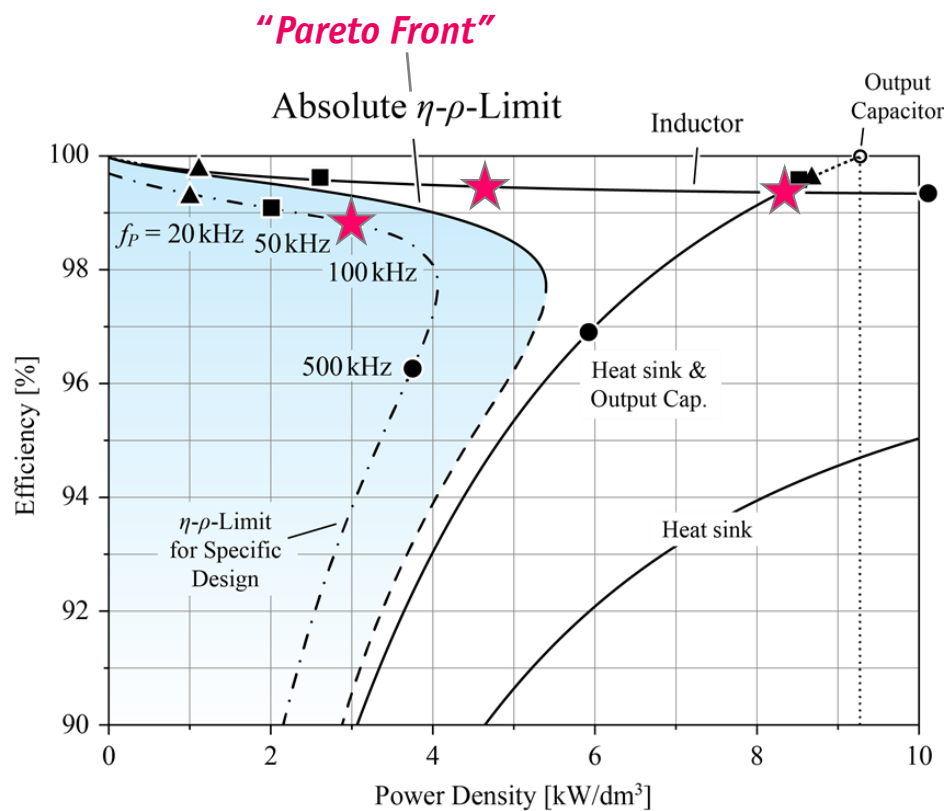
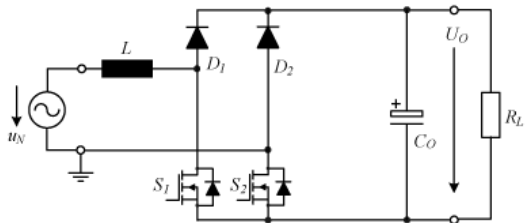
■ Only η - ρ -Pareto Front Allows Comprehensive Comparison of Converter Concepts (!)



► Determination of the η - ρ -Pareto Front (b)

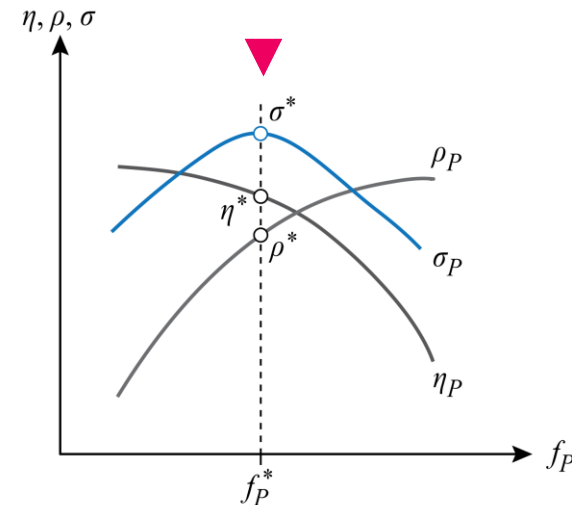
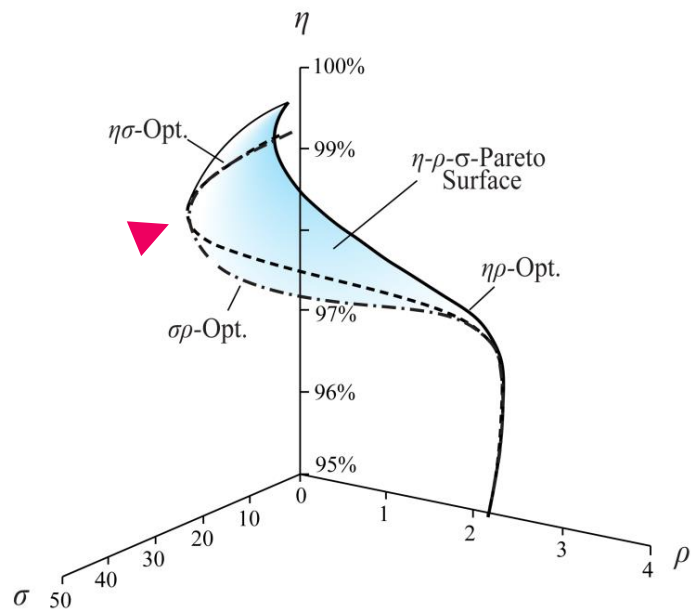
- Example: Consider Only f_p as Design Parameter
- Only the Consideration of All Possible Designs / Degrees of Freedom Clarifies the Absolute η - ρ -Performance Limit

★ $f_p = 100\text{kHz}$



► Converter Performance Evaluation Based on η - ρ - σ -Pareto Surface

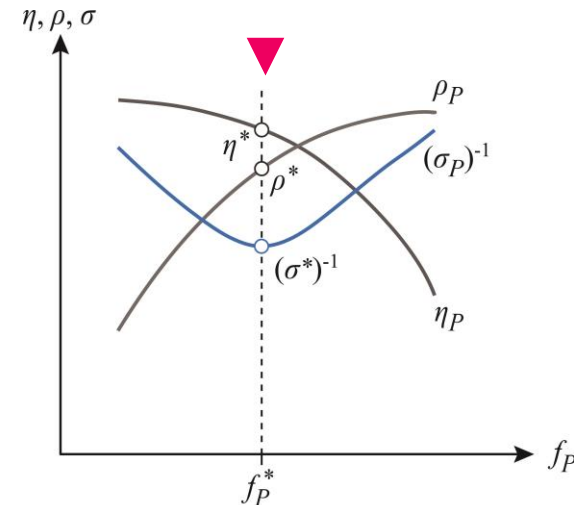
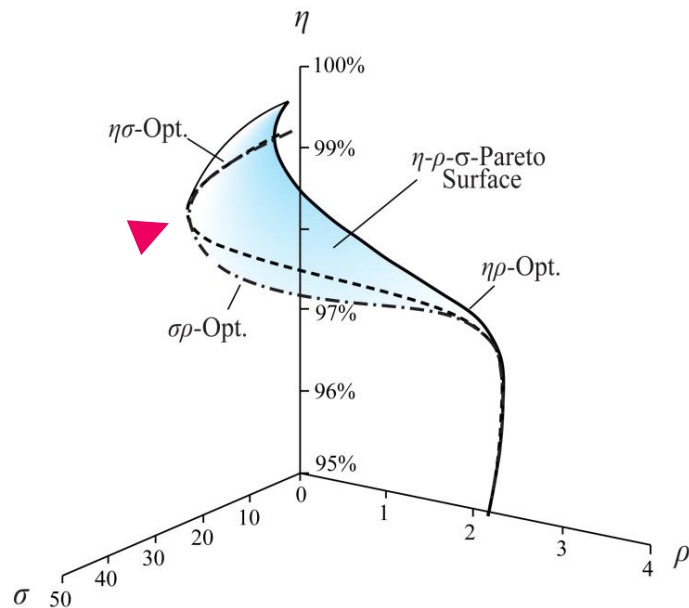
- Definition of a Power Electronics *"Technology Node"* $\rightarrow (\eta^*, \rho^*, \sigma^*, f_P^*)$
- Maximum σ [kW/\$], Related Efficiency & Power Density



- \rightarrow Specifying Only a Single Performance Index is of No Value (!)
- \rightarrow Achievable Perform. Depends on Conv. Type / Specs (e.g. Volt. Range) / Side Cond. (e.g. Cooling)

► Converter Performance Evaluation Based on η - ρ - σ -Pareto Surface

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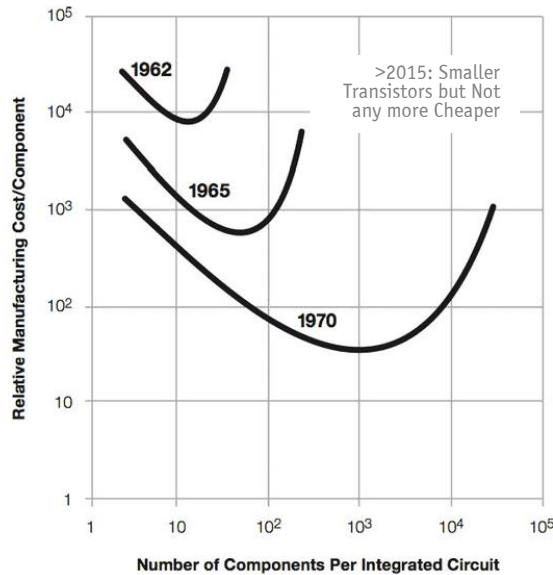


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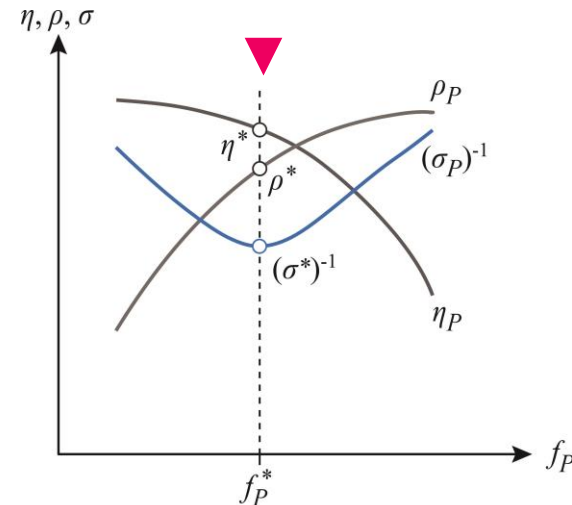
▶ Remark: Comparison to “Moore’s Law”

- “Moore’s Law” Defines Consecutive Techn. Nodes Based on Min. Costs per Integr. Circuit (!)
- Complexity for Min. Comp. Costs Increases approx. by Factor of 2 / Year

Economy of Scale → ← Lower Yield



Gordon Moore: The Future of Integrated Electronics, 1965 (Consideration of Three Consecutive Technology Nodes)



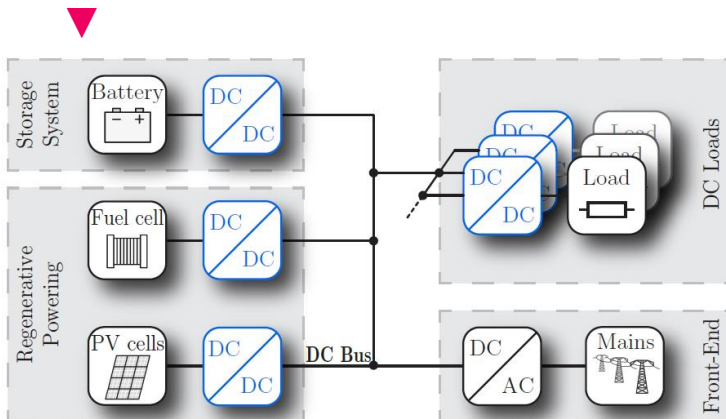
→ Definition of “ $\eta^*, \rho^*, \sigma^*, f_P^*$ -Node” Must Consider Conv. Type / Operating Range etc. (!)

Multi-Objective Optimization Application Example

*Comparative
Converter Evaluation*

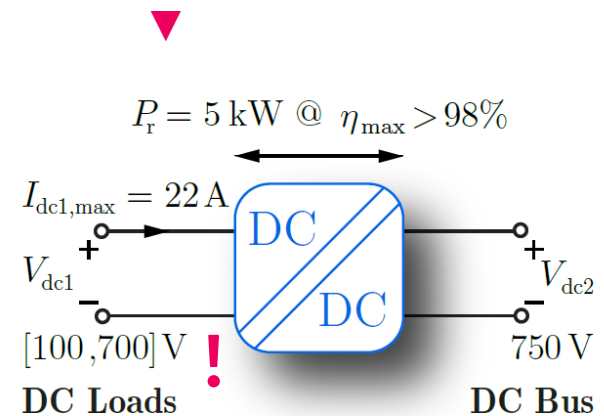
► Wide Input Voltage Range Isolated DC/DC Converter

Structure of "Smart Home" DC Microgrid



- Universal Isolated DC/DC Converter
- Bidirectional Power Flow
- Galvanic Isolation
- Wide Voltage Range
- High Partial Load Efficiency

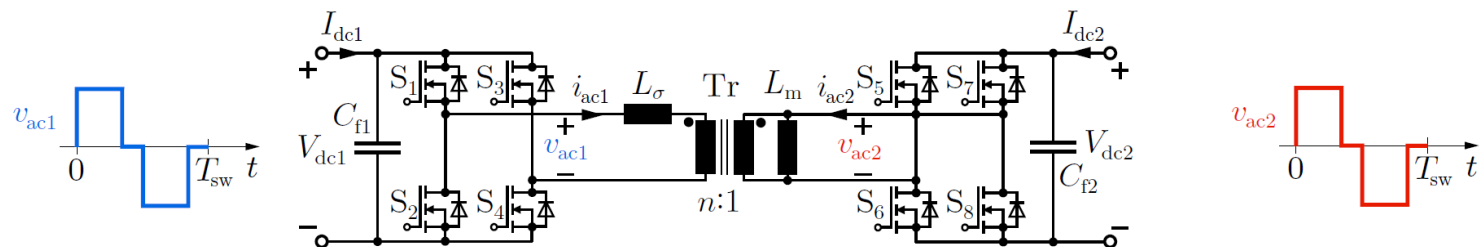
Universal DC/DC Converter



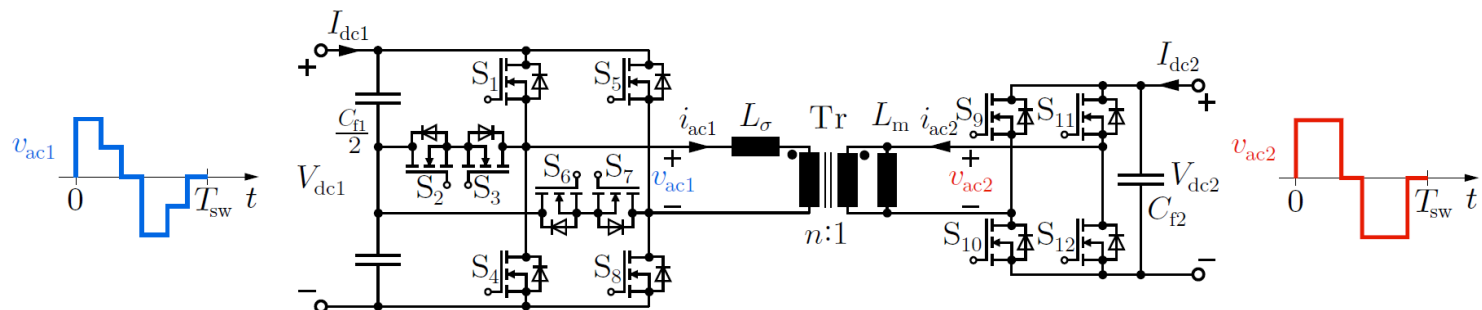
- Advantages
- Reduced System Complexity
- Lower Overall Development Costs
- Economy of Scale

► Comparative Evaluation of Converter Topologies

■ Conv. 3-Level Dual Active Bridge (3L-DAB)

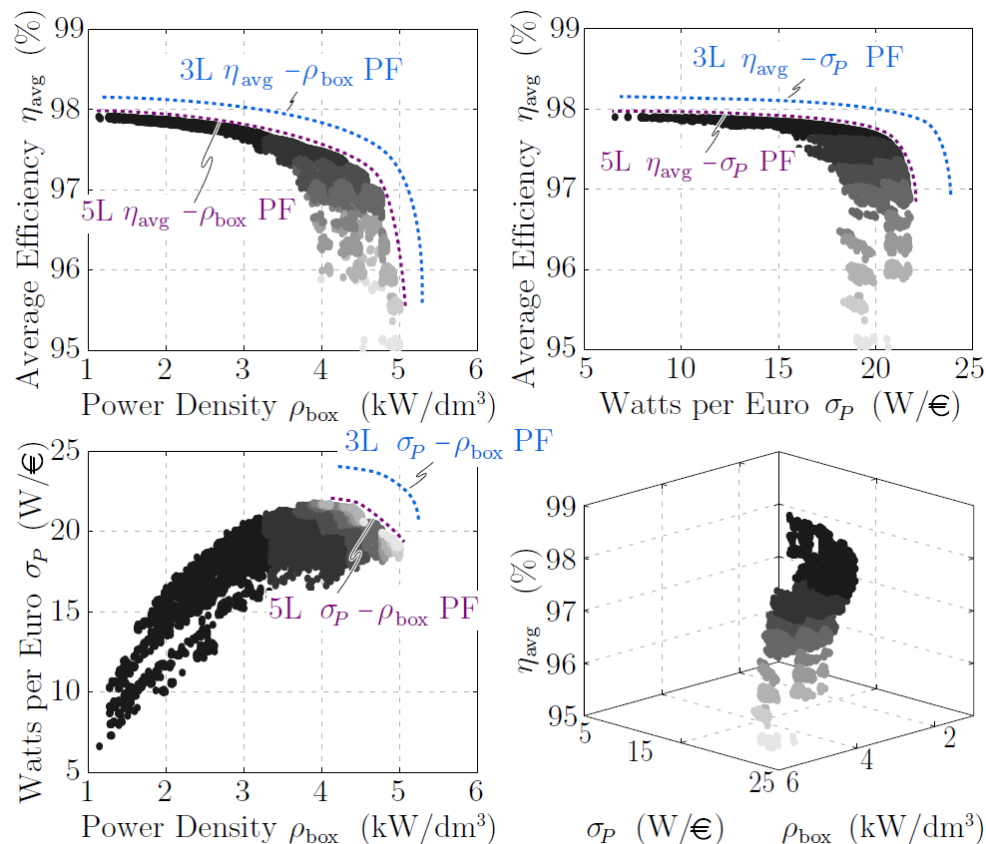
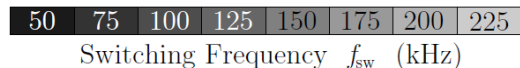


■ Advanced 5-Level Dual Active Bridge (5L-DAB)



► Optimization Results - Pareto Surfaces

- 3-Level Dual Active Bridge
- 5-Level Dual Active Bridge

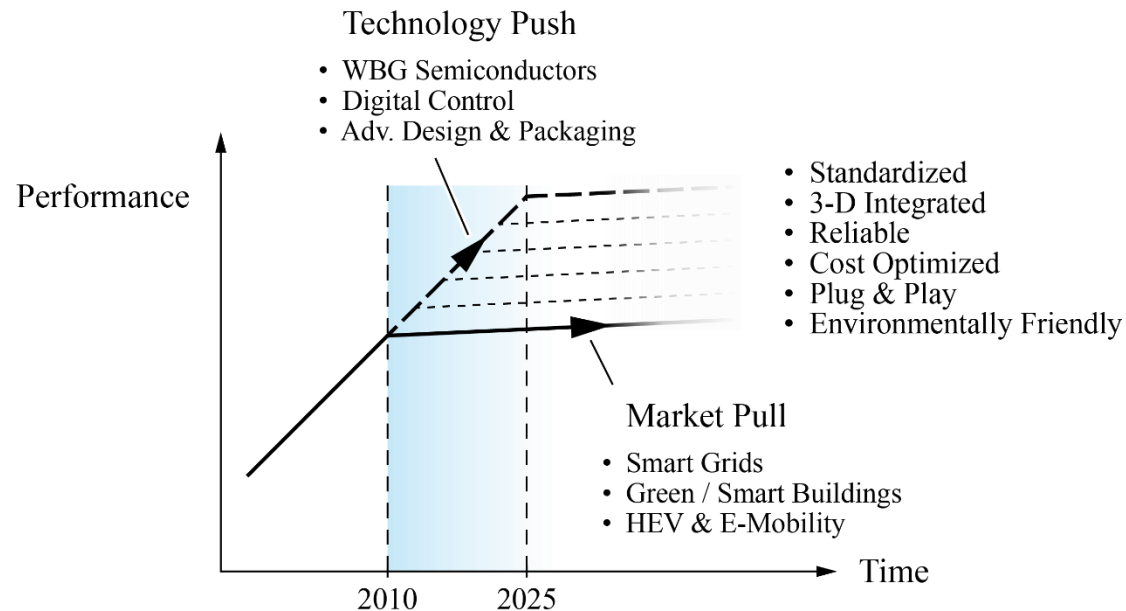


Conclusions

*Future Power Electronics Development
"Stairway to Heaven"*

► Future Development

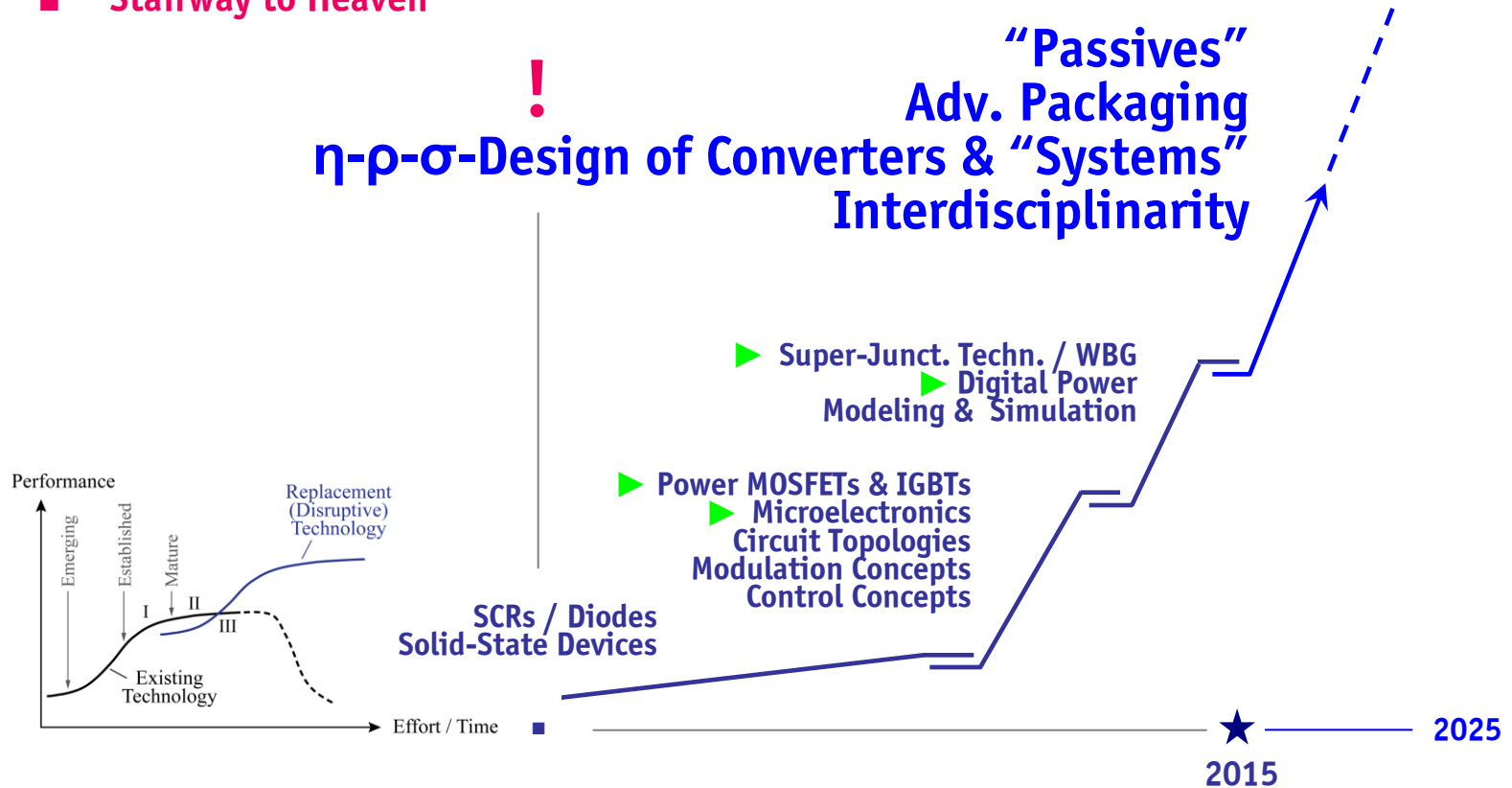
- **Megatrends – Renewable Energy / Energy Saving / E-Mobility / “SMART XXX”**
- **Power Electronics will Massively Spread in Applications**



- **More Application Specific Solutions**
- **Mature Technology – Cost Optimization @ Given Performance Level**
- **Design / Optimize / Verify (All in Simulation) - Faster / Cheaper / Better**

▶ Extrapolation of Technology S-Curve

■ "Stairway to Heaven"





■ Thank
You!