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Learning to Optimize: Training Deep Neural Networks for Interference Management

Haoran Sun∗, Xiangyi Chen ∗, Qingjiang Shi, Mingyi Hong, Xiao Fu, and Nicholas D. Sidiropoulos

I. INTRODUCTION

We consider an interference management problem for an interference channel consisting of $K$ single-antenna transceiver pairs. Let $h_{kk} \in \mathbb{C}$ denote the direct channel between transmitter $k$ and receiver $k$, and $h_{kj} \in \mathbb{C}$ denote the interference channel from transmitter $j$ to receiver $k$. Furthermore, we assume that the transmitted symbol of each transmitter $k$ is an independent Gaussian random variable with zero mean and variance $p_k$ (which is also referred to as the transmission power of transmitter $k$). Then the signal-to-interference-plus-noise ratio (SINR) for each receiver $k$ is given by

$$\text{SINR}_k = \frac{|h_{kk}|^2 p_k}{\sum_{j \neq k} |h_{kj}|^2 p_j + \sigma_k^2},$$

where $\sigma_k^2$ denotes the noise power at receiver $k$.

To optimally allocate power for each transmitter so that the weighted system throughput is maximized, the problem can be formulated as the following nonconvex problem

$$\max_{p_1, \ldots, p_K} \sum_{k=1}^{K} \alpha_k \log \left( 1 + \frac{|h_{kk}|^2 p_k}{\sum_{j \neq k} |h_{kj}|^2 p_j + \sigma_k^2} \right)$$

subject to $0 \leq p_k \leq P_{\max}$, $\forall k = 1, 2, \ldots, K$,

where $P_{\max}$ denotes the power budget of each transmitter and $\alpha_k$ denotes the nonnegative weight. Problem (1) is known to be NP-hard [1]. To obtain a good solution for problem (1), many transceiver design algorithms developed in the literature, such as WMMSE [2], SCALE [3], and the pricing algorithm [4]. A particular version of the WMMSE [5, Figure 1] also applied as WMMSE [2], SCALE [3], and the pricing algorithm [4]. A

II. THE LEARNING TO OPTIMIZE APPROACH

To resolve the computational issues arisen on the above interference management problem with stringent real-time requirements, we design a new “learning to optimize” based framework as shown in Figure 1. The main idea is to treat a given algorithm as a “black box”, and try to learn its input/output relation by using a deep neural network (DNN) [6]. If the nonlinear mapping can be learned accurately by a DNN of moderate size, then the interference management tasks can be performed in almost real time – since passing the input through a DNN only requires a small number of simple operations.

Unlike all existing works on approximating optimization algorithms such as those using unfolding [7]-[10], our approach is justified by rigorous theoretical analysis. We show that there are conditions under which an algorithm is learnable by a DNN [5], and indicate that it is possible to learn a well-defined optimization algorithm very well by using finite-sized deep neural networks. To concisely state the result, let us make the following definitions. Given an input channel vector $h := \{h_{ij}\} \in \mathbb{R}^{K^2}$, let us use $v(h)$ to denote the variable $v_i$ at $t^{th}$ iteration generated by WMMSE [5] (which basically represents $\sqrt{p_t}$ at $t^{th}$ iteration). Also let $H_{\min}, H_{\max} > 0$ denote the minimum and maximum channel strength and let $V_{\min} > 0$ be a given positive number. Let $\text{NET}(x, z)$ represent a neural network with $(x, z)$ as input.

**Theorem 1** Suppose that WMMSE is randomly initialized with $(v_i)^2 \leq P_{\max}, \sum_{i=1}^{V_{\min}} v_i^2 \geq V_{\min}$, and it is executed for $T$ iterations. Define the following set of ”admissible” channel realizations

$$\mathcal{H} := \left\{ h \mid H_{\min} \leq |h_{jk}| \leq H_{\max}, \forall j, k, \sum_{i=1}^{K} v_{i}(h) \geq V_{\min}, \forall t \right\}.$$  

Given $\epsilon > 0$, there exists a neural network with $h \in \mathbb{R}^{K^2}$ and $v^0 \in \mathcal{H}$ as input and $\text{NET}(h, v^0) \in \mathbb{R}^{K^2}$ as output, with the following number of layers

$$O\left(T^2 \log \left( \frac{\max(K, P_{\max}, H_{\max}, \frac{1}{\sigma}, \frac{1}{H_{\min}}, \frac{1}{P_{\min}}) + T \log \left( \frac{1}{\epsilon} \right) \right) \right)$$

and the following number of ReLUs and binary units

$$O\left(T^2 K^2 \log \left( \frac{\max(K, P_{\max}, H_{\max}, \frac{1}{\sigma}, \frac{1}{H_{\min}}, \frac{1}{P_{\min}}) + T \log \left( \frac{1}{\epsilon} \right) \right) \right).$$
such that the relation below holds true

$$
\max_{h \in \mathbb{H}} \max_{i} |(v(h))^T - N E T(h, v^0)_i| \leq \epsilon
$$

(2)

Remark 1 The bounds in Theorem 1 provide an intuitive understanding of how the size of the network should be dependent on various system parameters. A key observation is that having a neural network with multiple layers is essential in achieving our rate bounds. Another observation is that the effect of the approximation error on the size of the network is rather minor [the dependency is in the order of $O(\log(1/\epsilon))$]. However, we do want to point out that the numbers predicted by Theorem 1 represent some upper bounds on the size of the network. In practice, much smaller networks are often used to achieve the best tradeoff between computational speed and solution accuracy.

III. NUMERICAL RESULTS

To demonstrate the achievable performance of the proposed approach, a multi-cell interfering multiple Access Channel (IMAC) model is considered with a total of $N$ cells and $K$ users. In each cell, one BS is placed at the center of the cell and the users are randomly and uniformly distributed in the area; The channel between each user and each BS is randomly generated according to a Rayleigh fading distribution; see [5] for more detail. We perform the training and testing following the procedures outlined in Figure 1 and summarize the testing results in TABLE I. It can be seen that the proposed DNN approach can be trained to well-approximate the behavior of the state-of-the-art algorithm WMMSE [2], and achieve relatively high sum-rate performance. It is also shown that DNNs can achieve orders of magnitude speedup in computational time compared to state-of-the-art power allocation algorithms based on optimization.

Note that in the table, WMMSE (C)/(MATLAB) represents the WMMSE algorithm implemented using either C or MATLAB. The proposed DNN approach is implemented in Python 3.6.0 with TensorFlow 1.0.0 on one computer node with two 8-core Intel Haswell processors, two Nvidia K20 Graphical Processing Units (GPUs), and 128 GB of memory. The GPUs are used in the training stage to reduce the training time, but are not used in the testing stage.

<table>
<thead>
<tr>
<th># of base stations and users (N,K)</th>
<th>average sum-rate (bit/sec.)</th>
<th>WMMSE</th>
<th>DNN/WMMSE</th>
<th>total CPU time (sec.)</th>
<th>WMMSE(MATLAB)</th>
<th>DNN/WMMSE (C)</th>
<th>DNN/WMMSE (C)</th>
</tr>
</thead>
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<td>(3, 12)</td>
<td>17.722</td>
<td>18.028</td>
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<td>22.33</td>
<td>0.27</td>
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<td>20.606</td>
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<td>0.022</td>
<td>42.77</td>
<td>0.48</td>
<td>4.58%</td>
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<td>22.648</td>
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<td>0.025</td>
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<td>(7, 26)</td>
<td>33.513</td>
<td>35.453</td>
<td>94.53%</td>
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<td>140.44</td>
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<td>890.19</td>
<td>23.0</td>
<td>0.61%</td>
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</table>

References


